















- controls, and applications". IEEE Trans. on Ind. Elect. Vol. 49, n. 4, pp. 724- 738, 2002.
- [6] M. A. Hannan, Z. Abd Ghani, A. Mohamed, M. N. Uddin, "Real-Time Testing of a Fuzzy-Logic-Controller-Based Grid-Connected Photovoltaic Inverter System," in Industry Applications, IEEE Transactions on , vol.51, n. 6, pp.4775-4784, Nov.-Dec. 2015.
- [7] A. Araque G., J. L. Diaz R., O. E. Gualdrón G. THD optimization of a single phase multilevel inverter using genetic algorithms. Revista Colombiana de Tecnologías de Avanzada, Vol. 21, N. 1, 2013.
- [8] Fengjiang Wu; Bo Sun; Jiandong Duan; Ke Zhao, "Online Variable Topology-Type Photovoltaic Grid-Connected Inverter", IEEE Trans. on Industrial Electronics, vol. 62, no. 8, pp. 4814-4822, Aug. 2015
- [9] L. D. Pabon F., J. L. Diaz Rodriguez and E. A. Arevalo, "Multilevel power converter with variable frequency and low constant total harmonic distortion", IEEE 5th Colombian Workshop on Circuits and Systems (CWCAS), p.1-6, 2014.
- [10] J. L. Diaz Rodriguez, L. D. Pabon Fernandez and A. Pardo Garcia, "THD improvement of a PWM cascade multilevel power inverters using genetic algorithms as optimization method", WSEAS Transactions on Power Systems, vol. 10, pp. 46-54, 2015.
- [11] A. Nabae, I. Takahashi and H. Akagi, "A new neutral point clamped PWM inverter", IEEE Trans. Ins. Appl., vol. IA-17, N. 5, Sep-Oct, 1981, pp. 518-523.
- [12] L. D. Pabon Fernandez, E. A. Caicedo Peñaranda and J. L. Díaz Rodríguez, "Comparative Analysis Of 9 Levels Cascade Multilevel Converters With Selective Harmonic Elimination", IEEE Workshop in Power Electronics and Power Quality Applications (PEPQA), Bogota, pp.1-6, 2015.
- [13] M.R Banaei, H. Khounjahan, E. Salary, "Single-source cascaded transformers multilevel inverter with reduced number of switches," in Power Electronics, IET , vol.5, no.9, pp.1748-1753, Nov. 2012.
- [14] M. Malinowski, K. Gopakumar, J. Rodriguez and M. Pérez, "A Survey on Cascaded Multilevel Inverters", IEEE Trans. on Ind. Elect., Vol. 57, N. 7, pp. 2197-2206, July 2010.
- [15] R. H. Baker and H. B. Lawrence, Electric Power Converter, US. Patent Number 3,867,643, 1975.
- [16] A. Nabae, I. Takahashi and H. Akagi, "A new neutral point clamped PWM inverter", IEEE Trans. Ins. Appl., vol. IA-17, N. 5, pp. 518-523, Sep-Oct, 1981.
- [17] J. Rodriguez, J. S. Lai and F. ZhengPeng, "Multilevel inverters: a survey of topologies, controls, and applications". IEEE Trans. on Ind. Elect. Vol. 49, n. 4, pp. 724- 738, 2002.
- [18] J. Chavarria, D. Biel, F. Guinjoan, C. Meza, J. J. Negroni, "Energy-Balance Control of PV Cascaded Multilevel Grid-Connected Inverters Under Level-Shifted and Phase-Shifted PWMs," in Industrial Electronics, IEEE Transactions on , vol.60, n. 1, pp.98-111, Jan. 2013.
- [19] M. R. Banaei, H. Khounjahan and E. Salary, "Single-source cascaded transformers multilevel inverter with reduced number of switches". IET Power Electronics 5 (9), pp. 1748- 1753.
- [20] A. K. Panda and Y. Suresh, "Research on cascade multilevel inverter with single DC source by using three-phase transformers", Int. Journal of Electrical Power & Energy Systems, vol. 40, n. 1, 2012, pp. 9-20.
- [21] Ye Zhang, Yun Wei Li, N. R. Zargari, Zhongyuan Cheng, "Improved Selective Harmonics Elimination Scheme With Online Harmonic Compensation for High-Power PWM Converters", IEEE Trans. on Power Electronics, vol.30, n. 7, pp.3508-3517, 2015.
- [22] G. Nageswara Rao, P. Sangameswara Raju, K. Chandra Sekhar, Harmonic elimination of cascaded H-bridge multilevel inverter based active power filter controlled by intelligent techniques, International Journal of Electrical Power & Energy Systems, Volume 61, Pages 56-63, October 2014.
- [23] Ke Shen; Dept. of Electr. Eng., Harbin Inst. of Technol., Harbin, China ; Dan Zhao ; Jun Mei ; Leon M. Tolbert, "Elimination of Harmonics in a Modular Multilevel Converter Using Particle Swarm Optimization-Based Staircase Modulation Strategy," in IEEE Transactions on Industrial Electronics, vol. 61, no. 10, pp. 5311-5322, Oct., 2014.
- [24] B. Ozpineci, L. M. Tolbert and J. N. Chiasson, "Harmonic optimization of multilevel converters using genetic algorithms," in IEEE Power Electronics Letters, vol. 3, n. 3, pp. 92-95, Sept. 2005.
- [25] J. L. Diaz Rodriguez, L. D. Pabon F. and E. A. Caicedo, "Novel methodology for the calculation of transformers in power multilevel converters", Ingeniería y Competitividad, vol. 17, n. 1, pp. 121-132, 2015.