

# Optimal Embedded System for Two-Axis Tracking PV Panels

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*Abstract:* - A PV tracker system is a solution one of those methods able to increase the PV power generation. Theoretical, a PV tracker system with two-axis, can increase the overall solar energy capture about 45%, compared to a fixed PV module tilted at an angle equal to the local latitude. An one-axis tracking system, the increase is approximately 32%.

In this paper, we design and implement a two-axis tracking system of photovoltaic (PV) systems, who follows the maximum power point (MPP) using a programmable circuit XILINX type Complex Programmable Logic Device- CPLD and Xilinx ISE software. Thus, PV module will reach its MPP in relation date and time of the day. The test bed relies on an algorithm integrated in the XILINX who has as inputs: date, location's latitude and longitude, the standard longitude (related to the location's position in relation with Greenwich), and the number the positions of the Sun's path.

To establish the position of the panel in a time of day value is determined by the following calculations: the angle of the day, correction factor of Earth's orbit, the solar declination angle, the equation of time in minutes, eastern time using latitude angle, the number of hours the Sun shines using angle eastern time, time the Sun sets, vectors containing the coordinates of the positions of the Sun (in this case 10 positions) during the day and azimuth angle.

*Key-Words:* - Renewable sources, PV systems, PV Tracker, programmable circuit, Complex Programmable Logic Device.

## 1 Introduction

Renewable energy sources (RES) are getting more and more widespread, mainly due to the fact that they generate energy by keeping the environment clean. Theirs rapid evolution of RES during the last two decades materialised in a lot of RES power systems all over the world. A disadvantage of these power plants is the high cost of the installation. In this respect, the approaches trying to optimise the design of these ones are well wellcome. However, such an effort requires detailed knowledge, e.g. the meteorological data of the site where the system will be installed and operational results from similar systems, if available. [1] and [2]

A PV tracker system is a solution one of those methods able to increase the PV power generation. Theoretical, a PV tracker system with two-axis, can increase the overall solar energy capture about 45%, compared to a fixed PV module tilted at an angle

equal to the local latitude. An one-axis tracking system, the increase is approximately 32%. [3]

In this paper, we design and implement a two-axis tracking system of photovoltaic (PV) systems, who follows the maximum power point (MPP) using a programmable circuit XILINX type Complex Programmable Logic Device - CPLD and Xilinx ISE software. Thus, PV module will reach its MPP in relation date and time of the day. The test bed relies on an algorithm integrated in the XILINX who has as inputs: date, location's latitude and longitude, the standard longitude (related to the location's position in relation with Greenwich), and the number the positions of the Sun's path.

Sun path refers to the apparent significant seasonal-and-hourly positional changes of the sun (and length of daylight) as the Earth rotates, and orbits around the sun. The relative position of the sun is a major factor in the performance of solar energy systems. Accurate location-specific knowledge of

sun path and climatic conditions is essential for economic decisions about solar collector area, orientation, landscaping, summer shading, and the cost-effective use of solar trackers. To gather solar energy effectively, a PV panel should be within about twenty degrees either side of perpendicular to the sun.

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## 2 System Architecture

Positioning on directions east-west and north-south of a solar panel (that is on a trajectory sun tracking device) is reduced to ordering automatic motor on the two axes of orientation system (Fig. 1).

Position of the sun was calculated using formulas to approximate azimuth and elevation angles of the sun.

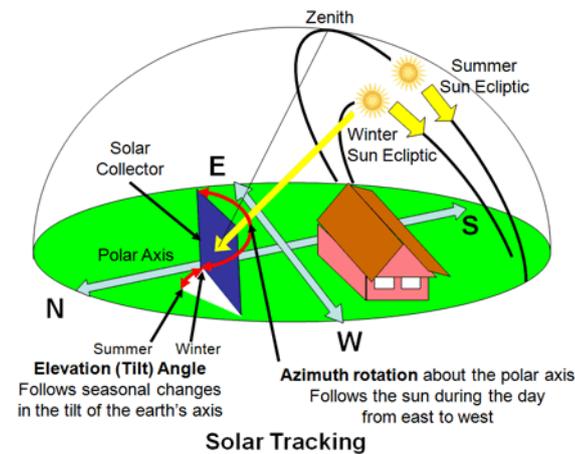


Fig. 1. Elevation angle and azimuth rotation [4]

To design and develop a prototype positioning for photovoltaic solar panels. Performance conditions are useful table axis transducers to be 25 kg, angular displacement speed up to 2 rev/min and limit switch heads with hardware method.

Mechanical assembly for the vertical axis of rotation of the solar panel complies with kinematics scheme in Fig. 2. In order to choose the electric motor you must know the operating conditions (work schedule, temperature and humidity environment), the required power and the rotation speed of the motor shaft. The motor-gearbox chosen to drive the axis is GP232004, manufactured by Baldor

enterprise. The engine consumption must be low, and its torque must be multiplied by the mechanical transmissions [5].

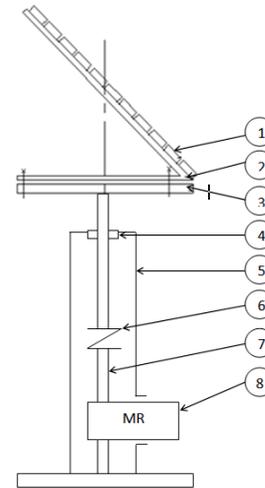


Fig. 2. Cinematic scheme

Assembly components are described below:

1. Photovoltaic
2. Support photovoltaic
3. Turntable
4. Roller bearing (thrust bearing)
5. Housing / frame
6. Flexible coupling
7. Tree
8. Motor-gearbox group

The formula for calculating the approximate azimuth angle is shown in equation (1), where:  $\Phi_s$  – azimuth angle;  $\theta_s$  – elevation angle;  $\delta$  – declination of the sun;  $\Phi$  – local latitude.

$$\cos \Phi_s = \frac{\sin \delta - \sin \theta_s \sin \Phi}{\cos \theta_s \cos \Phi} \quad (1)$$

Elevation angle is negative when the sun drops below the horizon. This angle may be approximated by the equation (2), where:  $h$  - hour angle;  $\theta_s$  - elevation angle;  $\delta$  - declination of the sun;  $\Phi$  - local latitude. [6]

$$\sin \theta_s = \cos h \cos \delta \cos \Phi + \sin \delta \sin \Phi \quad (2)$$

The hour angle of a point is the angle between two planes: one containing the Earth's axis and the zenith (the meridian plane), and the other containing the Earth's axis and the given point (the hour circle passing through the point). The angle may be expressed as negative east of the meridian plane and positive west of the meridian plane, or as positive westward from  $0^\circ$  to  $360^\circ$ . The angle may be measured in degrees or in time, with  $24h = 360^\circ$  exactly. For example for 10:30 AM, hour angle is -22.5 degrees (according to equation (3)).

$$h = (\text{Time}-12) * 15 + \text{minutes} * 0.25 \quad (3)$$

The declination of the sun is the angle that the sun's rays make it with equatorial plane. This angle is shown in equation (4), where: N represents number the day from current year and is calculated according to the equation (5).

$$\delta = 23.44^\circ \sin \left[ \frac{360^\circ}{365} (N + 284) \right] \quad (4)$$

$$N = \text{int}(275 * \text{month}/9) - 2 * \text{int}((\text{month}+9)/12) + \text{day} - 30 \quad (5)$$

The software was made in XILINX ISE and implements the algorithm for calculating the two angles of the sun shows two paintings:

- one for location data and is made up of several fields, for the following data: latitude, data from calendar (day / month / year) and local time in 24h format, hour GMT + 2 (hour / minute);
- and the second is for the results, and gives the calculated values of the declination angle, elevation, azimuth, azimuth east, sunrise in minutes and number of days of the year.

With this algorithm we can calculate the position of the sun for every day of the year with an good precision, so the voltage debited to photovoltaic panel to be high. Due to mechanical implementation of solar tracker, which can move on east-west direction up to 270 grade was able to simulate the longest day of the year which was on June 21 (the summer solstice).

### 2.1 Xilinx ISE and Complex Programmable Logic Devices Board

Software XILINX ISE (Integrated Software Environment) is the ideal solution for CPLD and FPGA design offering HDL synthesis and simulation, implementation, device fitting, and JTAG programming. To achieve an application can use schematic description or hardware description languages (VHDL or Verilog).

With this program can be achieved a synthesis applications, which means behavioral model transformation made in a hardware description language (VHDL or Verilog) in a circuit structure.

A complex programmable logic device (CPLD) is a programmable logic device with complexity between that of PALs and FPGAs, and architectural features of both. The main building block of the CPLD is a macrocell, which contains logic implementing disjunctive normal form expressions and more specialized logic operations. [6]

CPLDs have used analog sense amplifiers to boost the performance of their architectures. This

performance boost came at the cost of very high current requirements. CoolRunner-II CPLDs, created by Xilinx, use an innovative all-digital core to achieve the same levels of performance at ultra-low power requirements. This allows designers to use the same CPLD architecture for both high-performance and low-power designs.

CoolRunner-II CPLDs are the latest CPLD product offering from Xilinx. CoolRunner-II CPLDs combine high performance with low power operation. Standby current on CoolRunner-II CPLD devices is less than 100 $\mu$ A.

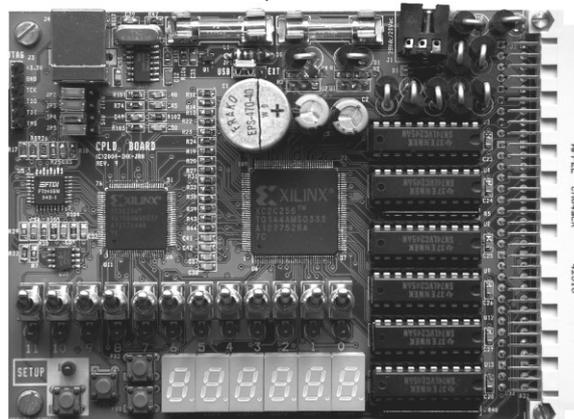


Fig. 3. CPLD board

### 3 Application Description

Of all the elements that are available on board CPLD (Xilinx CoolRunner-II CPLD with 256 macrocells XC2C256-TQ144-7), in application presented are accessed following:

- 12 switches;
- 6 displays 7-segment (they will be displayed on the desired values);
- 1 button (reset), if we want the panel to return to its original position;
- clock signal which is variable ("ClkSlow") and the value is set at 1000 Hz in accordance with a setting in the conFigation menu, then this value is used twice, is divided by two separators watch: one to obtain a correct display (visualization devices all appear to be lit at the same time) and one to obtain a delay when the panel moves from one position to another.

The main modules of the application are shown in Fig. 4.

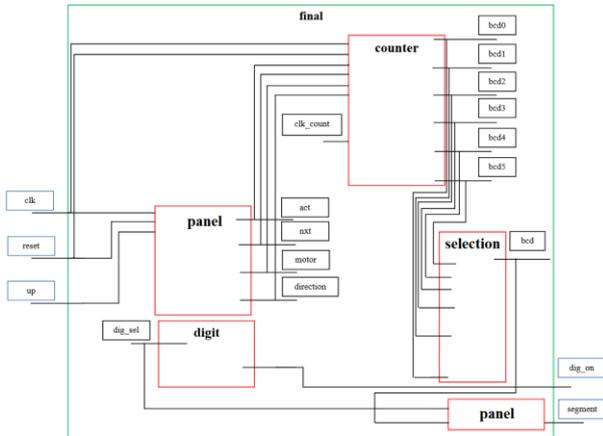


Fig. 4. The main modules of the application

The panel will have 10 positions and may submit the following states:

- waiting in a position for change the input, i.e. waiting for activation the sensor corresponding with the motor in position stop; the panel in mod initial is in position 1, that position being standard position;
- the photovoltaic panel can move from East to West, given command with help of the ten switches, or from West to East, that move will be achieved without an intervention when the panel reached its final position;
  - the 10 buttons on the CPLD board, will be used for "up".
  - the 6 displays on the CPLD board, will be used:
    - 2 for position number (0-10);
    - 3 for panel direction (ra - moving from East to West, ar - moving from West to East, PS - when it is in a certain position pending);
    - 1 for motor status (P - on, O - off).

In Fig. 5 are shown some states of the panel, and in Fig. 6 are shown a few examples of transition states and change the values of the variables used in the program.

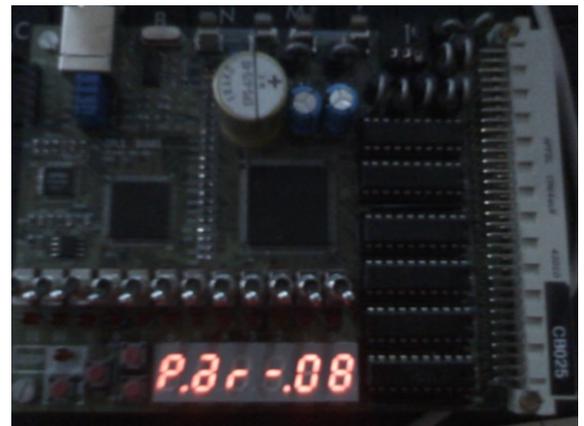
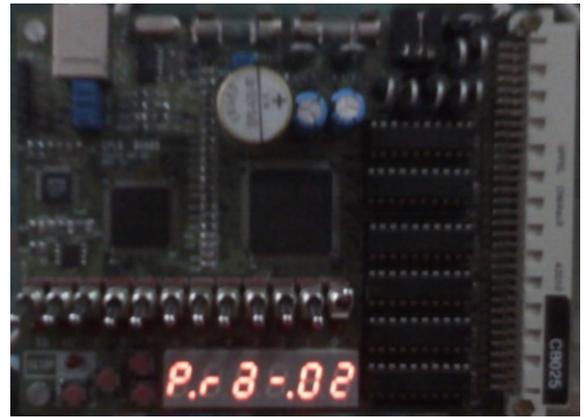
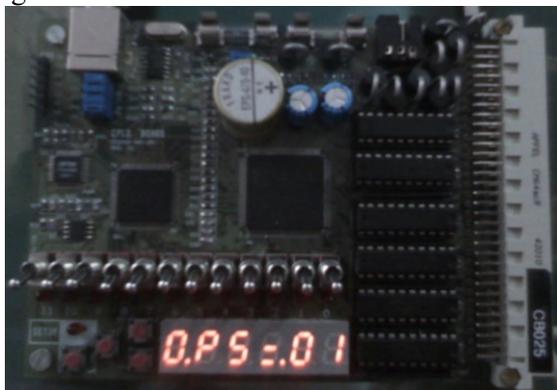


Fig. 5. Representing the first state, the movement to the second position and the state of recovery of the panel

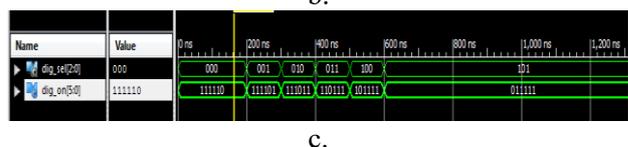
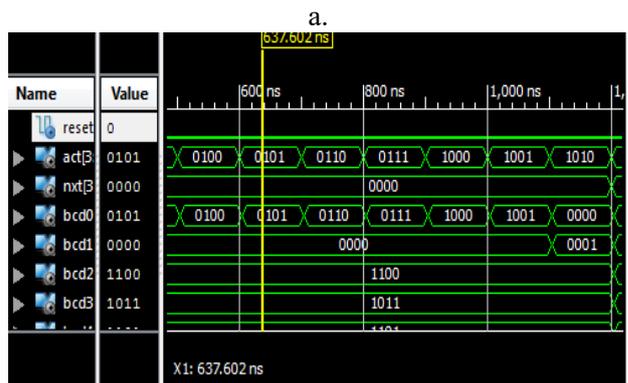
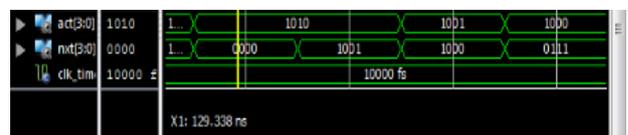


Fig. 6. a. transition states; b. change of values bcd; c. change the variable dig\_on

## 4 Experimental Results

In Fig. 7 is shown experimental application "PV solar tracker" what describe the motion tracking the Sun trajectory.



Fig. 7. Power obtained by acquisition from photovoltaic panel

In the Fig. 8 we have represented the power (P) debited of photovoltaic panel. Period the monitoring is in between the hours 07:05 and 17:35 in 17 February 2016.

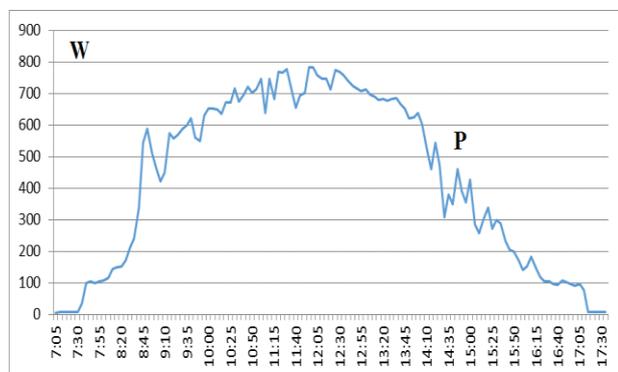


Fig. 8. Power obtained by acquisition from photovoltaic panel

## 4 Conclusion

The article solved an innovative problem related to solar energy capture systems and conversion into electricity. Permanent positioning of the solar panel after MPP point increases overall system efficiency. A photovoltaic tracker system is a solution one of those methods able to increase the PV power generation.

The objective of the program made in Xilinx IDE it was to describe the motion of a photovoltaic panel tracking the Sun trajectory. Thus, there was obtained a prototype of an algorithm for two-axis tracking photovoltaic panel, who follows the MPP using a programmable circuit board (CPLD board) and Xilinx ISE software. Thus, photovoltaic panel will reach its MPP in relation date and time of the day. In a further development it is necessary to consider some aspect, such as the possibility of erroneous information from the sensors, due to adverse weather conditions.

The system will have an impact on the educational and research environment, being installed in the Multidisciplinary Science and Technology Research Institute of Valahia University of Targoviste and serving as teaching material for students of the Faculty of Electrical Engineering - Valahia University of Targoviste.

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